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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/724,597	11/28/2000	Peter L. Rosefield	ATI010002	7653	
75	90 11/19/2002				
J. Gustav Larson Simon, Fakhoury, Tangalos, Frantz & Galasso, PLC P.O. Box 26503			EXAMINER		
			SOWARD, IDA M		
Austin, TX 78755-0503			ART UNIT	PAPER NUMBER	
			2822		
			DATE MAILED: 11/19/2002		

Please find below and/or attached an Office communication concerning this application or proceeding.

,		Application No.	Applicant(s)	-1
		09/724,597	ROSEFIELD ET AL.	1
,	Office Action Summary	Examiner	Art Unit	
		Ida M Soward	2822	
Period fo	The MAILING DATE of this communication app or Reply	pears on the cover sheet with the o	correspondence address	,
THE I - Exter after - If the - If NO - Failu	ORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. Insions of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period or to reply within the set or extended period for reply will, by statute eply received by the Office later than three months after the mailing dipatent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be tir y within the statutory minimum of thirty (30) day vill apply and will expire SIX (6) MONTHS from , cause the application to become ABANDONE	mely filed /s will be considered timely. I the mailing date of this communicat (D) (35 U.S.C. § 133).	ion.
1)[🖂	Responsive to communication(s) filed on 05 S	September 2002 .		
2a)⊠	This action is FINAL . 2b) Th	is action is non-final.		
3) 🗌 Dispositi	Since this application is in condition for allowal closed in accordance with the practice under on of Claims			s is
i	Claim(s) 14-20 is/are pending in the applicatio	n		
'-	4a) Of the above claim(s) is/are withdraw			
	Claim(s) is/are allowed.	With the World and Control and		
·	Claim(s) <u>14-20</u> is/are rejected.			
i	Claim(s) is/are objected to.			
·	Claim(s) are subject to restriction and/or	r election requirement.		
1	on Papers	1		
9) 🔲 🖯	The specification is objected to by the Examine	r		
10) 🔲 🛚	The drawing(s) filed on is/are: a) ☐ accep	oted or b) objected to by the Exa	miner.	
,	Applicant may not request that any objection to the	e drawing(s) be held in abeyance. S	ee 37 CFR 1.85(a).	
11) 🔲 🏾	The proposed drawing correction filed on	is: a) approved b) disappro	oved by the Examiner.	
	If approved, corrected drawings are required in rep	bly to this Office action.		
12) 🔲 7	The oath or declaration is objected to by the Exa	aminer.		
Priority u	nder 35 U.S.C. §§ 119 and 120			
13)	Acknowledgment is made of a claim for foreign	priority under 35 U.S.C. § 119(a)-(d) or (f).	
a)[☐All b)☐ Some * c)☐ None of:		•	
	1. Certified copies of the priority documents	s have been received.		
	2. Certified copies of the priority documents	s have been received in Applicati	on No	
1	 Copies of the certified copies of the prior application from the International Bur ee the attached detailed Office action for a list of 	reau (PCT Rule 17.2(a)).		
B) (1	cknowledgment is made of a claim for domestic			ition).
	☐ The translation of the foreign language pro			,
	cknowledgment is made of a claim for domestic			
Attachment	(s)			
2) D Notice	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Informal F	r (PTO-413) Paper No(s) Patent Application (PTO-152)	. •
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DETAILED ACTION

This Office Action is in response to Applicants' response filed September 5, 2002.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 14 and 17-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Prior Art Figures 1-3 in view of Kameda et al. (6,130,484).

Prior Art Figures 1-3 teach an apparatus comprising: semiconductor substrate having an input output (I0) ring, the I0 ring having a bond pad portion and an active buffer portion; the bond pad portion including: a first bond pad; a second set of bond pads having one or more bond pads; and a third bond pad, wherein the second set of bond pads is immediately adjacent to the first and third bond pads. Prior Art Figures 1-3 further teach the second set of bond pads including one or more bond pads. However, Prior Art Figures 1-3 fail to teach a conductive trace coupling the first bond pad to the third bond pad. Kameda et al. teach a conductive trace 3a coupling a first bond pad 5 to a third bond pad 11b (Figures 1-2, col. 3, lines 37-65). Kameda et al. further teaches a package substrate having a power portion; a first bond wire 7 connected to the first

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bond pad 5 and the power portion; a second bond wire 3a connected to the third bond pad 11b and the power portion, wherein one of the first bond pad and the third bond pad being connected to the active buffer portion 2 of the 10 ring. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the structure of Prior Art Figures 1-3 with the conductive trace of Kameda et al. to improve the reliability of the integrated circuit IC.

Claims 15-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Prior Art Figures 1-3 and Kameda et al. (6,130,484) as applied to claim 14 above, and further in view of Lebby et al. (5,5543,958) and Ngai et al. (US 2001/0010471 A1). Kameda et al. further teach a first bond pad 5 and a third bond pad 4 being power pads (Figures 1-2, col. 3, lines 37-65). However, Prior Art Figures 1-3 and Kameda et al. fail to teach a power pad coupled to a fixed voltage source, wherein the fixed voltage source is one of Vdd and Vss. Lebby et al. teach a power pad 27 coupled to a fixed voltage source (Figure 1, col. 4, lines 42-46). Ngai et al. teach a fixed voltage source is one of Vdd and Vss (page 5, paragraph [0062]). Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the structure of Prior Art Figures 1-3 and the conductive trace of Kameda et al. to improve the reliability of the integrated circuit IC with the power pad of Lebby et al. and the fixed voltage source of Ngai et al. to provide high performance interconnects.

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Response to Arguments

Applicant's arguments filed September 5, 2002 have been fully considered but they are not persuasive. Kameda et al. clearly teach in off the corners of Figure 1 a second set of bond pads 1 immediately adjacent to the first (closest to corners) and third bond pads 4 and there is a conductive trace (in stripes) coupling the first bond pad to the third bond pad.

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ida M Soward whose telephone number is 703-305-

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3308. The examiner can normally be reached on Monday - Thursday, 6:30 am to 5:00

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pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 703-308-4905. The fax phone numbers for the organization where this application or proceeding is assigned are 703-872-9318 for regular communications and 703-872-9319 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

ims

November 17, 2002

AMIR ZARABIAN
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800